

PATENT NUMBER

**U.S. UTILITY Patent Application**

12W SCANNED	O.I.P.E. BXD Q.A. MC	PATENT-DATE
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APPLICATION NO. 09/517987	CONT/PRIOR D	CLASS <del>437</del> 438	*SUBCLASS 320	ART UNIT <del>1756</del>	EXAMINER Booth
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APPLICANTS  
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TITLE  
Method of forming planarized structures in an integrated circuit

PTO-2040  
12/99

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS RECEIVED</b>	
	<b>Sheets Drawn</b>	<b>Figs. Drawn</b>	<b>Print Fig.</b>	<b>Total Claims *      Paid Claim in U.S.</b>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____  _____	_____ (Primary Examiner)		<b>ISSUE FEE</b>	
	_____ (Date)		<b>Amount Due</b>	<b>Date Paid</b>
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		<b>ISSUE BATCH NUMBER</b>	

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